IN THE SPECIFICATION

Replace the title with the following:

ALLOCATION OF PROCESSOR BANDWIDTH BY INSERTING INTERRUPT
SERVICING INSTRUCTIONS TO INTERVENE MAIN PROGRAM IN INSTRUCTION
QUEUE MECHANISM

On page 1, beginning at line 3, please insert the following paragraph:

This application is a continuation of prior U.S. Serial No. 09/474,834, filed December
30, 1999.

At page 4, line 13, before the Detailed Description, insert —Fig. 5 is a system block
diagram illustrating a computer system that incorporates an embodiment of the invention—.

Replace the Abstract in its entirety with the following replacement Abstract that is
additionally provided at page 11 on a separate sheet.

—The method and apparatus feature detecting an interrupt service request; inserting interrupt
servicing instructions responsive to the interrupt service request into an instruction queue
mechanism in such manner that the inserted interrupt servicing instructions intervene
mainline program instructions within a queue mechanism thereby allocating core processor
bandwidth between the inserted interrupt servicing instructions and the mainline program
instructions; and processing the instructions within the instruction queue mechanism
including the mainline program instructions and the inserted interrupt servicing instructions.
By doing so, core processor bandwidth allocation between the mainline program instructions
and the inserted interrupt servicing instructions is achieved by concurrent in-line staging of
the same within the instruction queue mechanism.—